



Srsti Quantum Private Limited

STUDENT PROJECT PARTICIPATION AND COMPLETION LETTER

Date: 23 October 2026

Reference No.: SQPL/SPC/2026/1023/001

TO WHOMSOEVER IT MAY CONCERN

This is to certify that **Master TATA MAITHRINATH**, a student of Class XII (CBSE) at **Delhi Public School, Vijayawada, Andhra Pradesh, India**, undertook a guided technology project with Srsti titled **"Introduction to Quantum Fabrication: Semiconductor Processes and Quantum Device Engineering."**

The project was designed to introduce the student to the emerging field of quantum hardware fabrication and provide an understanding of how semiconductor manufacturing techniques, materials science and precision engineering contribute to the development of quantum computing devices.

Under appropriate technical guidance, the student explored the following areas:

- 1 **Fundamentals of Quantum Hardware:** Studied the basic architecture of quantum computing devices and learned how different qubit technologies require specialised materials, fabrication methods and operating conditions.
- 2 **Introduction to Semiconductor Fabrication:** Learned the principles of microfabrication processes, including photolithography, thin-film deposition, etching and pattern transfer, and their relevance to the development of advanced electronic and quantum devices.
- 3 **Quantum Device Structures:** Explored the conceptual design of quantum devices, including superconducting quantum circuits, resonators, electrodes and associated structures used in selected quantum hardware platforms.
- 4 **Materials and Device Engineering:** Examined the importance of material properties, surface quality, dimensional precision and contamination control in the performance and reliability of quantum devices.
- 5 **Quantum Fabrication Process Flow:** Developed an introductory understanding of the sequence of processes involved in translating a device design into a fabricated structure, including design preparation, patterning, material deposition, inspection and testing.
- 6 **Device Characterisation and Testing:** Studied the purpose of electrical measurements, microscopy and other characterisation techniques used to assess device structures and evaluate fabrication quality.
- 7 **Technical Documentation and Project Presentation:** Prepared project documentation describing the fabrication concepts studied, process-flow observations, device-design principles and potential challenges in developing quantum hardware.

Through this project, Master TATA MAITHRINATH gained introductory exposure to the interdisciplinary foundations of quantum device fabrication, spanning semiconductor engineering, materials science, electronics and quantum technology.



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The project encouraged scientific curiosity, systematic problem-solving, technical understanding and an appreciation of the specialised engineering capabilities required to develop next-generation quantum computing hardware.

We appreciate his interest in semiconductor technology and quantum hardware engineering and encourage him to pursue further academic study and practical learning in these emerging fields.

We wish him every success in his future academic endeavours.

Yours sincerely,

For Srsti Quantum

K. Subhash



Subhash Kalidindi
Director of Nano Fabrication